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	Application / Conf. No.	10/666,669 / 8241		
TRANSMITTAL	Filing Date	Filing Date First Named Inventor Tim Tuan Examiner Name Daniel D. Chang Art Unit 2819 Patent No. Attorney Docket Number X-1462 US		
FORM	First Named Inventor			
(to be used for all correspondence after initial filing)	Examiner Name			
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Tim Tuan et al.

ssignee: Xilinx, Inc.

Title: "Disabling Unused/Inactive Resources in Programmable Logic Devices

for Static Power Reduction"

Serial No.: 10/666,669 File Date: 09/19/2003

Examiner: Daniel D. Chang Art Unit: 2819

Docket No.: X-1462 US Conf. No.: 8241

Mail Stop Amendment COMMISSIONER FOR PATENTS P.O. Box 1450

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RESPONSE TO THE SECOND RESTRICTION REQUIREMENT

Dear Sir:

This Response and Election is filed herewith in response to the Second Restriction Requirement dated August 4, 2005, which is set to expire on September 4, 2005.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 5 of this paper.